

Model Name: GA-H61M-DS2

Revision 2.01

SHEET

TITLE

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02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
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05	CPU_LGA1155-B
06	CPU_LGA1155-C
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08	DDR III CHANNEL B
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10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
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16	ITE 8728 LPC IO
17	COMA,KB_MS,R_USB,-PROCHOT
18	HWM,FAN CTRL,OV
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25	ATX,-S_WARN,-S_ACK,5VDUAL
26	Richtek_RT8120_CPU_VTT
27	VCORE INTERSIL_95836_1

28	VCORE INTERSIL_95836_2
29	VCORE INTERSIL_95836_3
30	LPT

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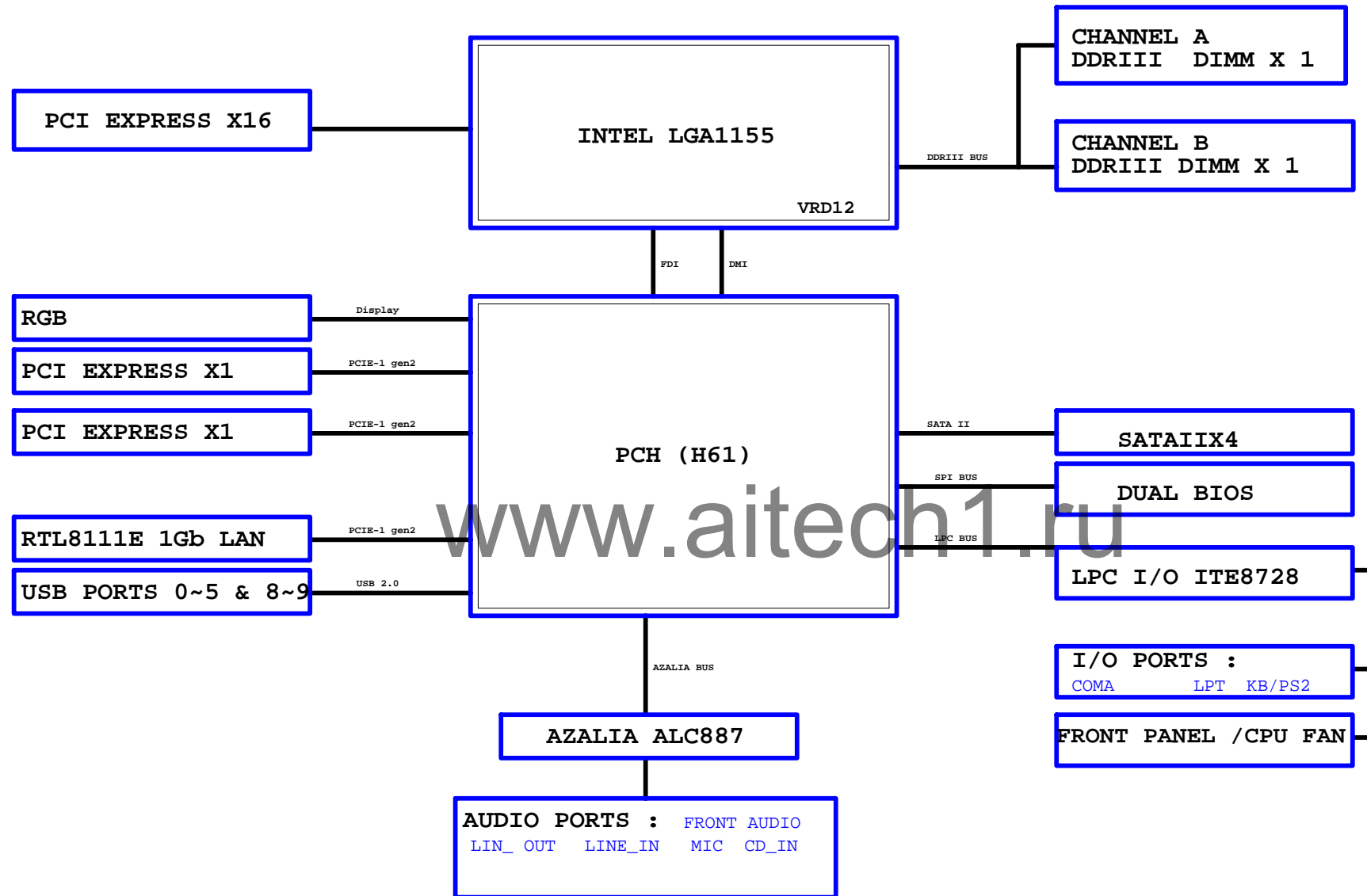
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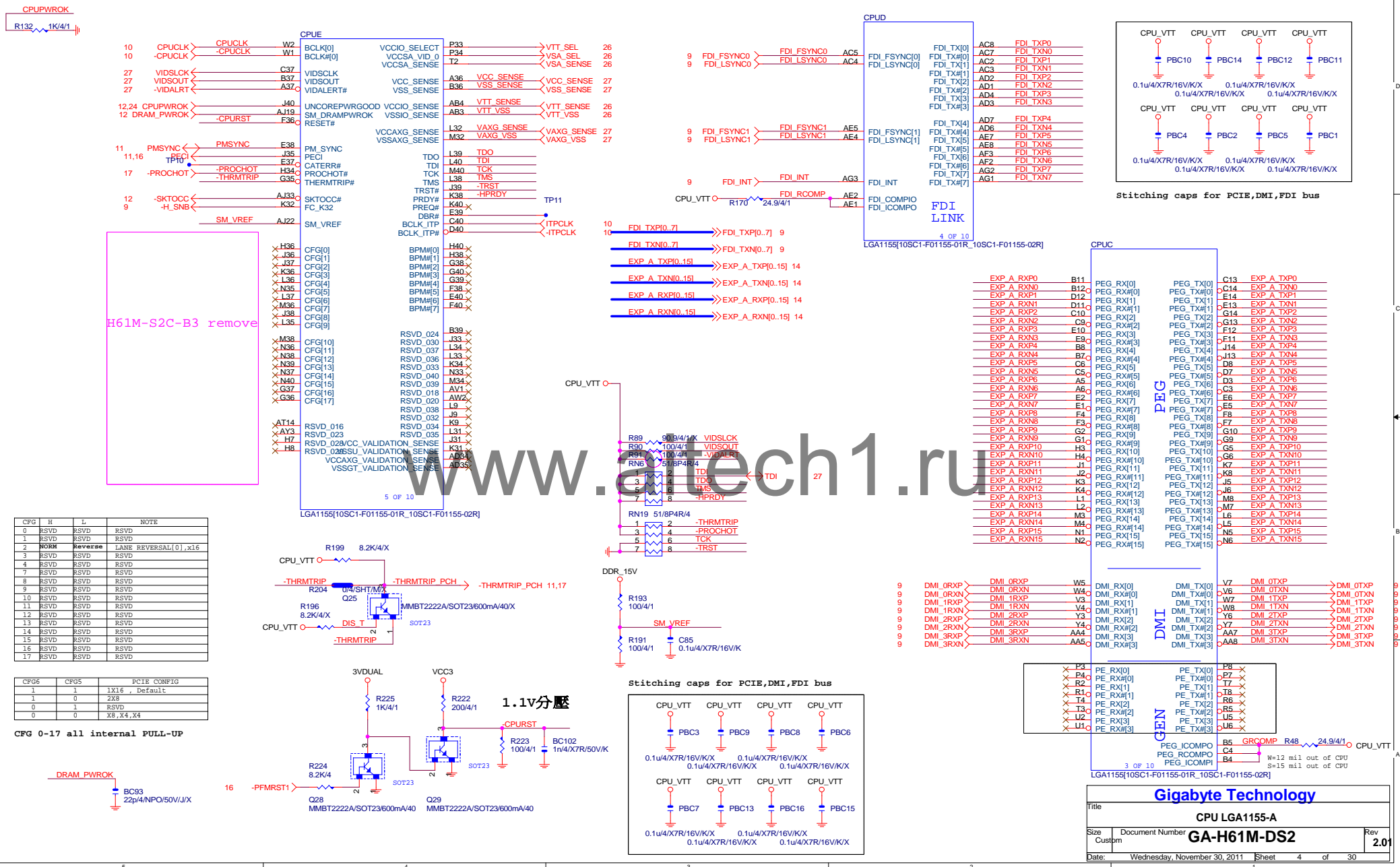
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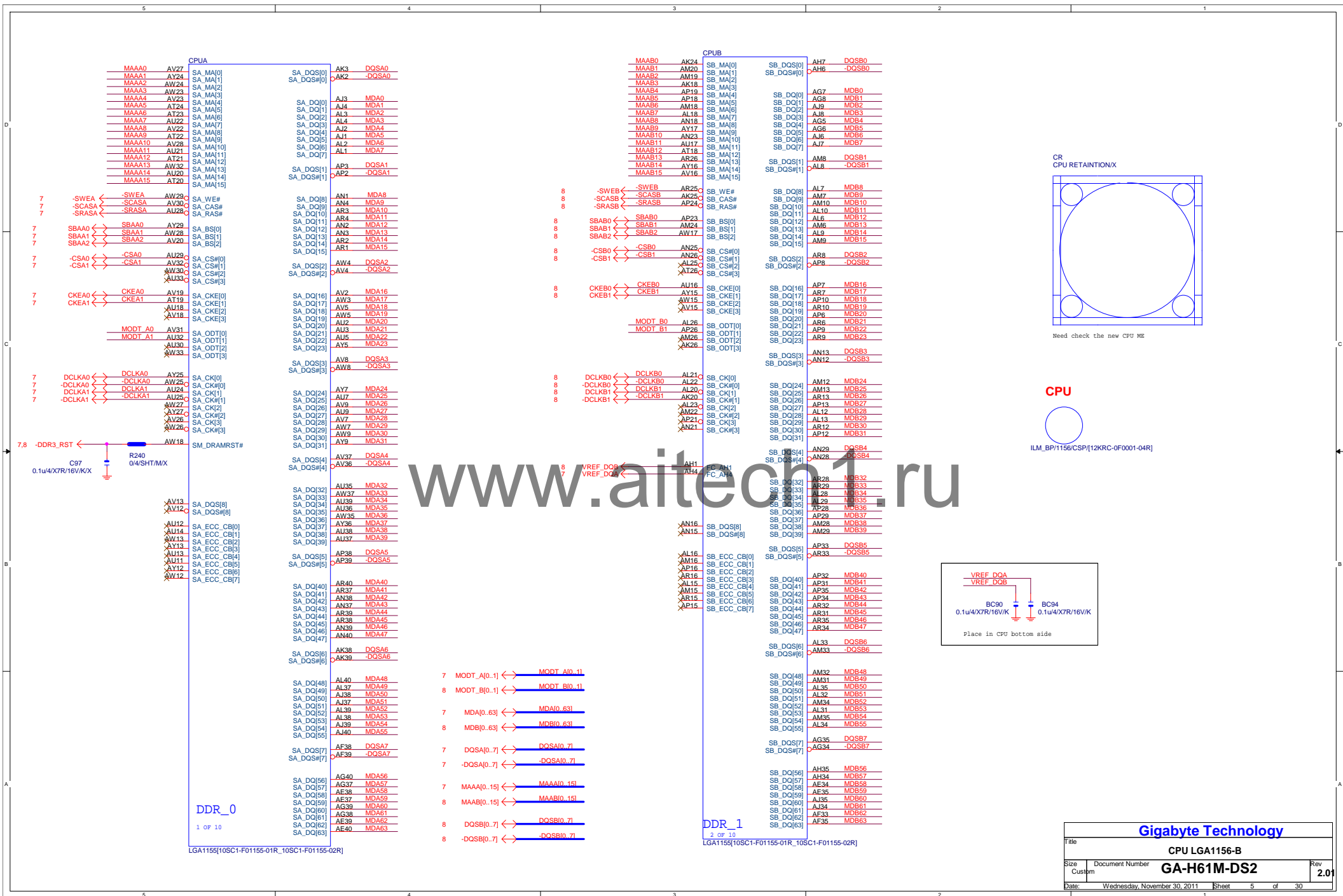
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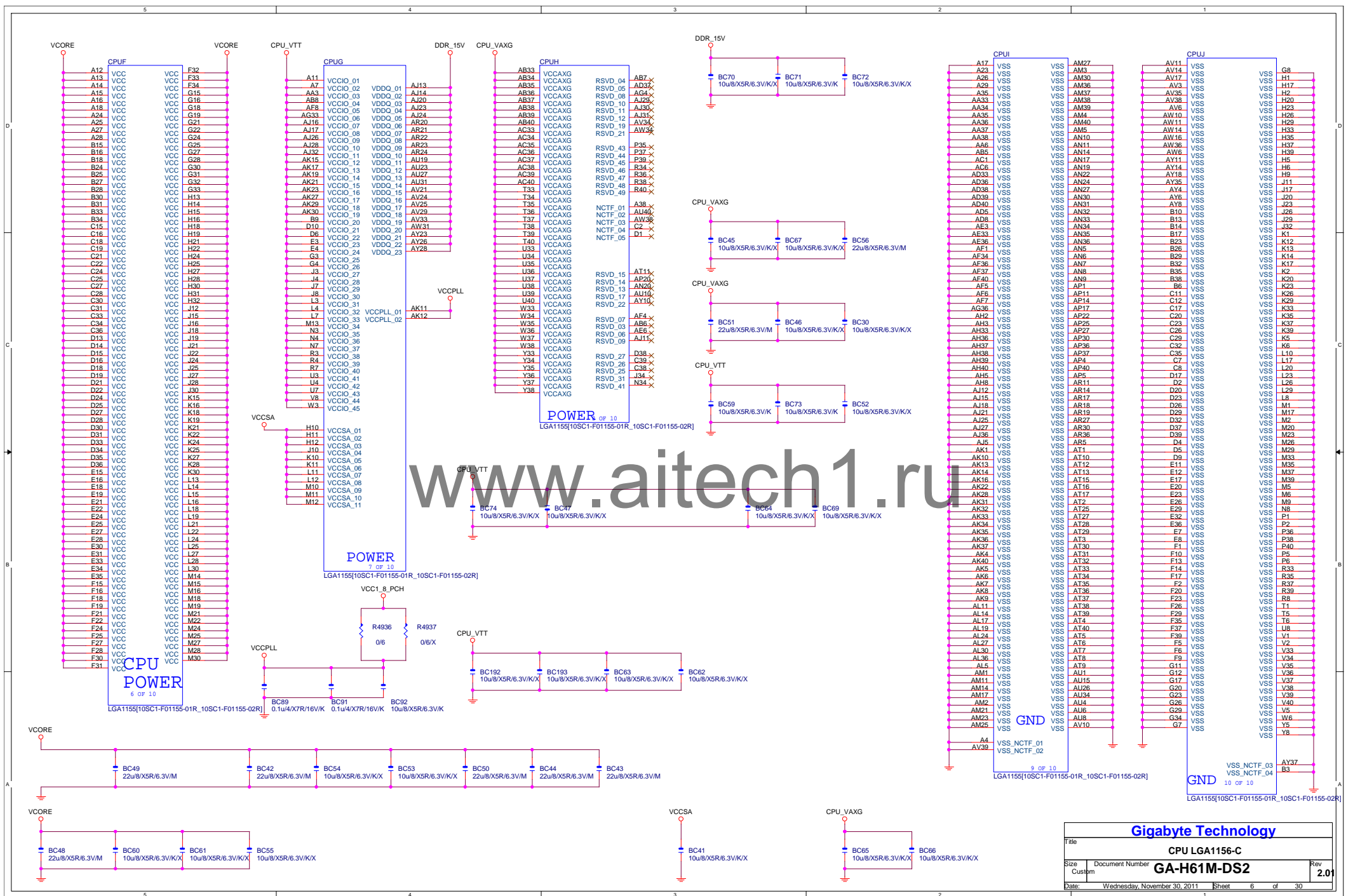
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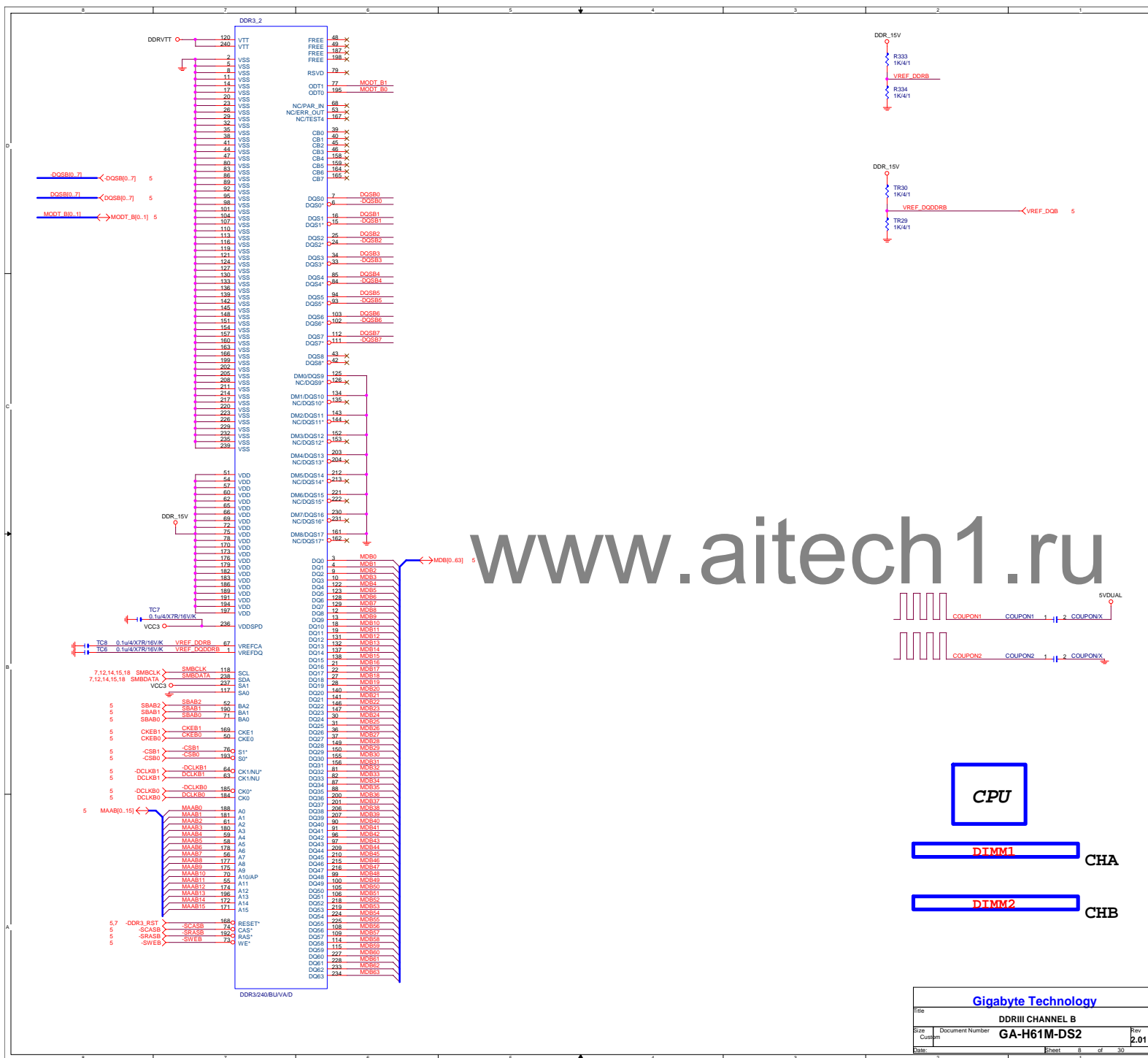
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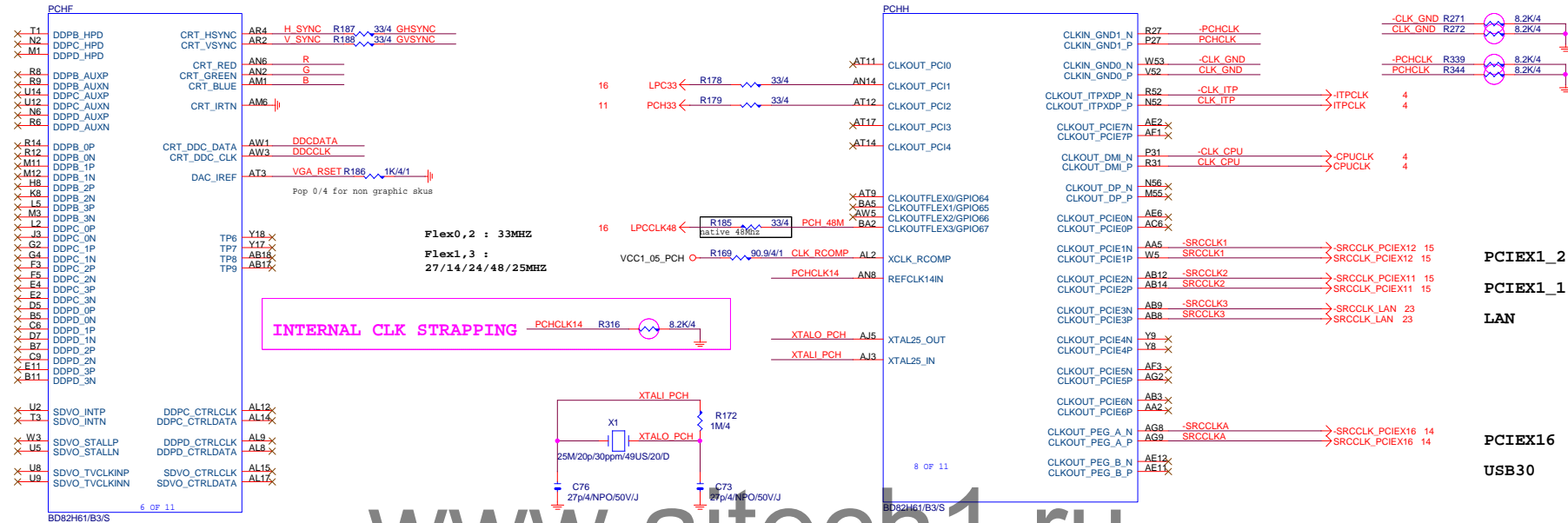




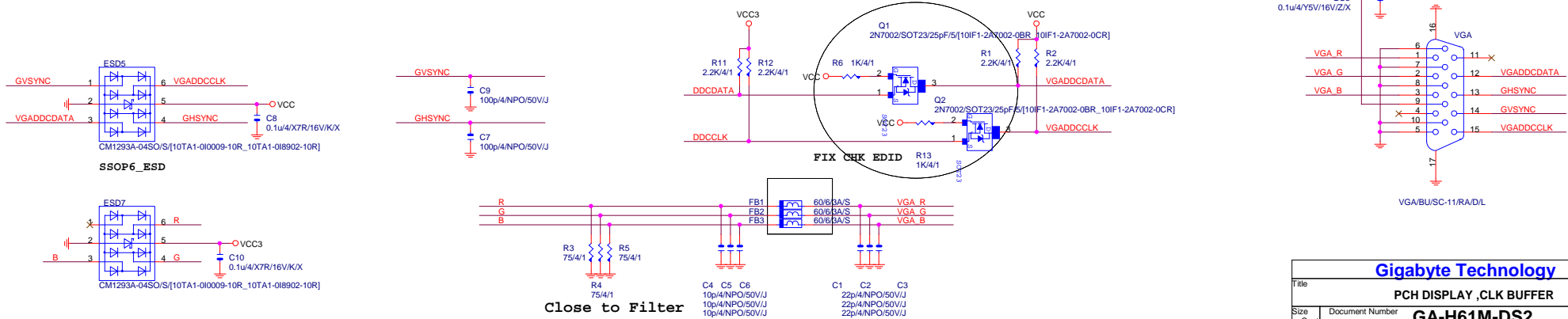








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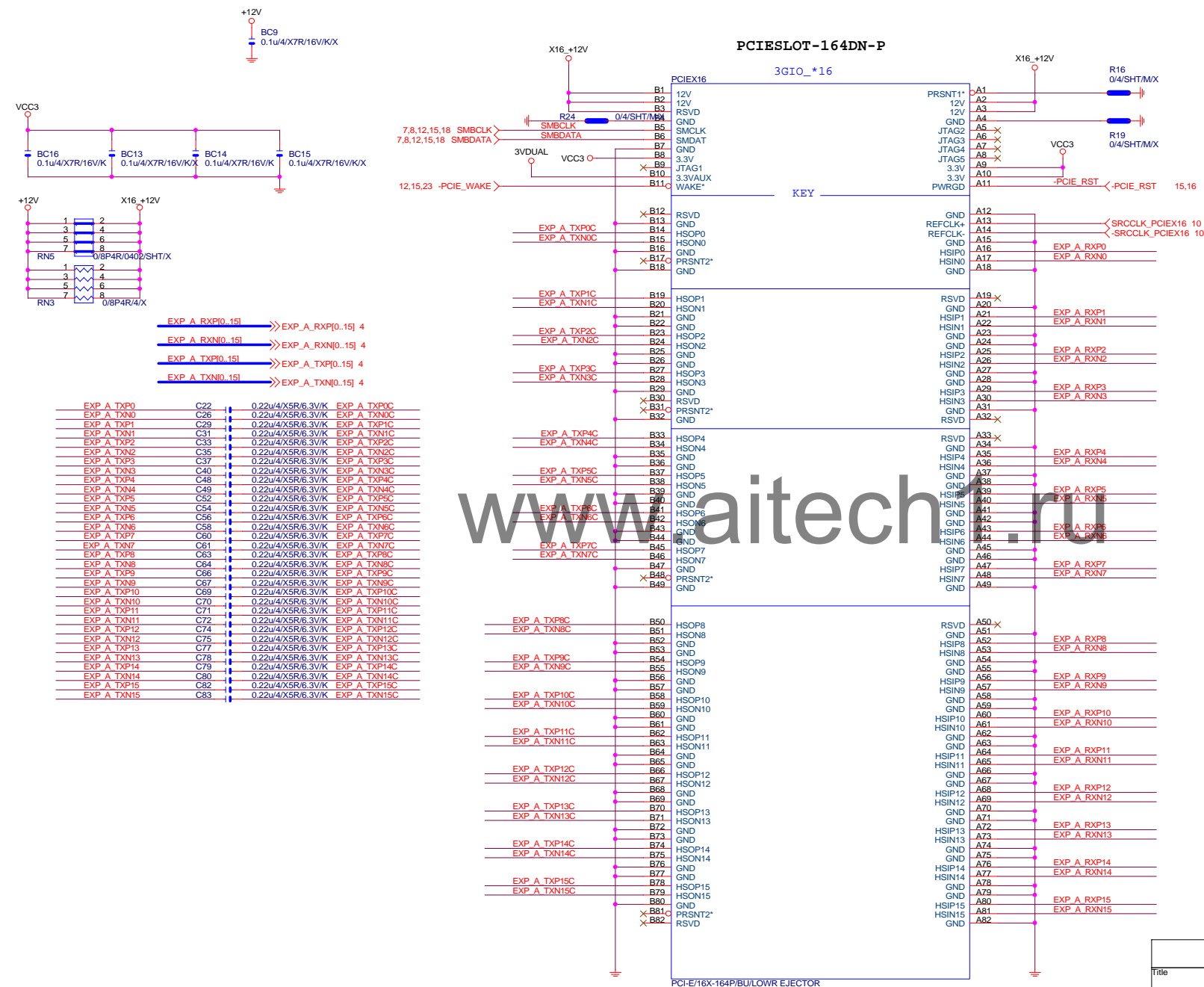








EC6D\_8D10MM-RH



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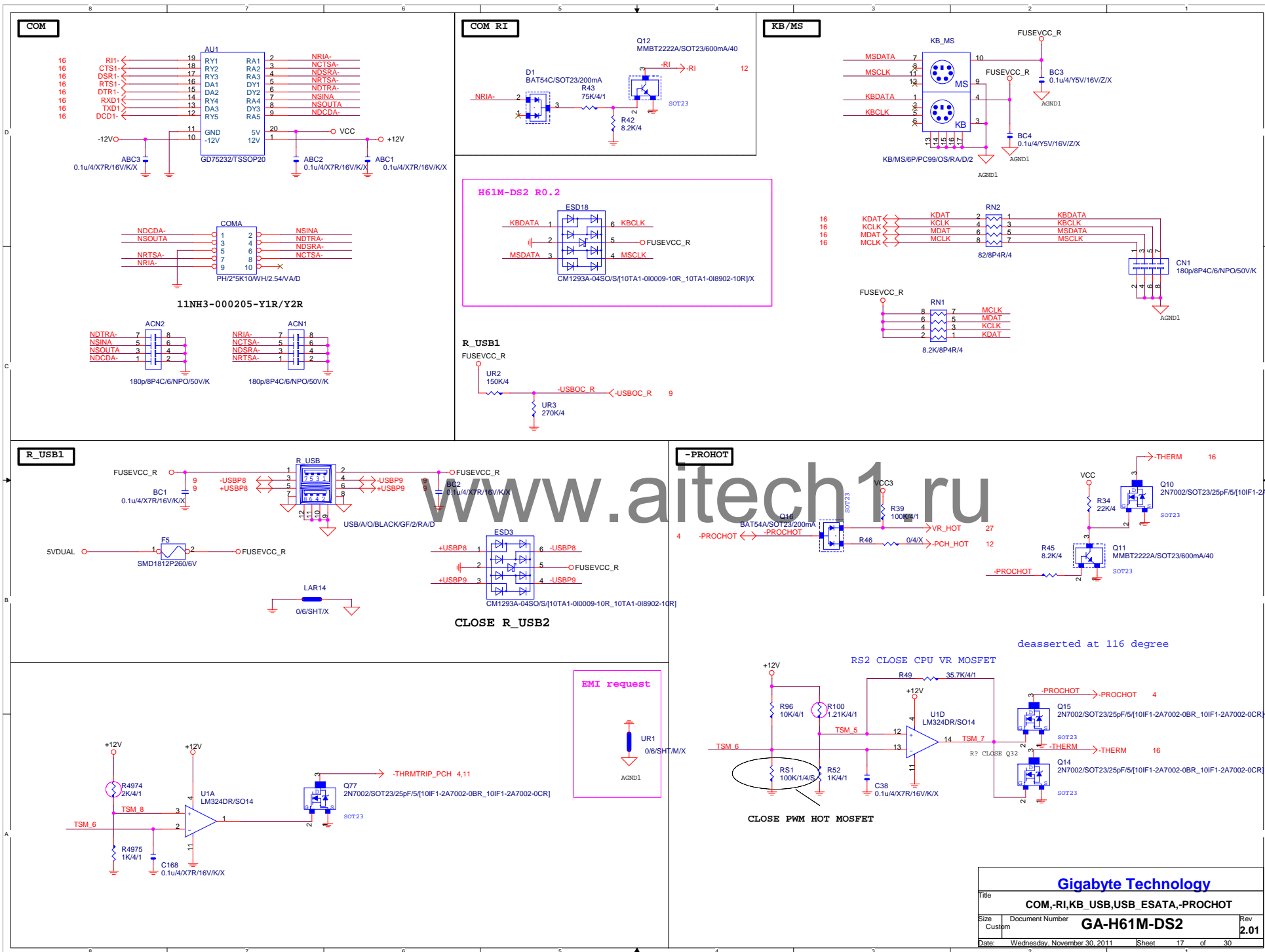
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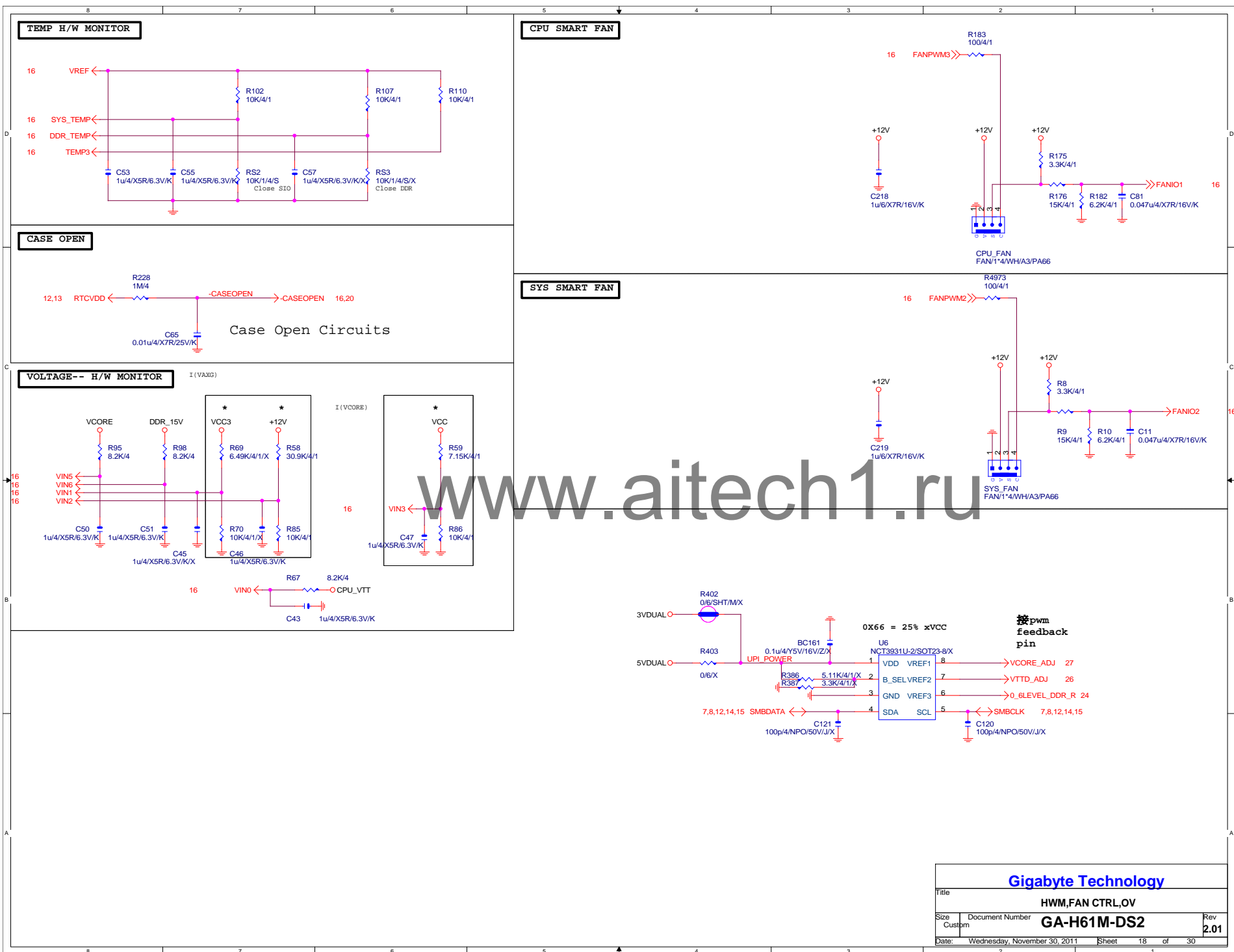




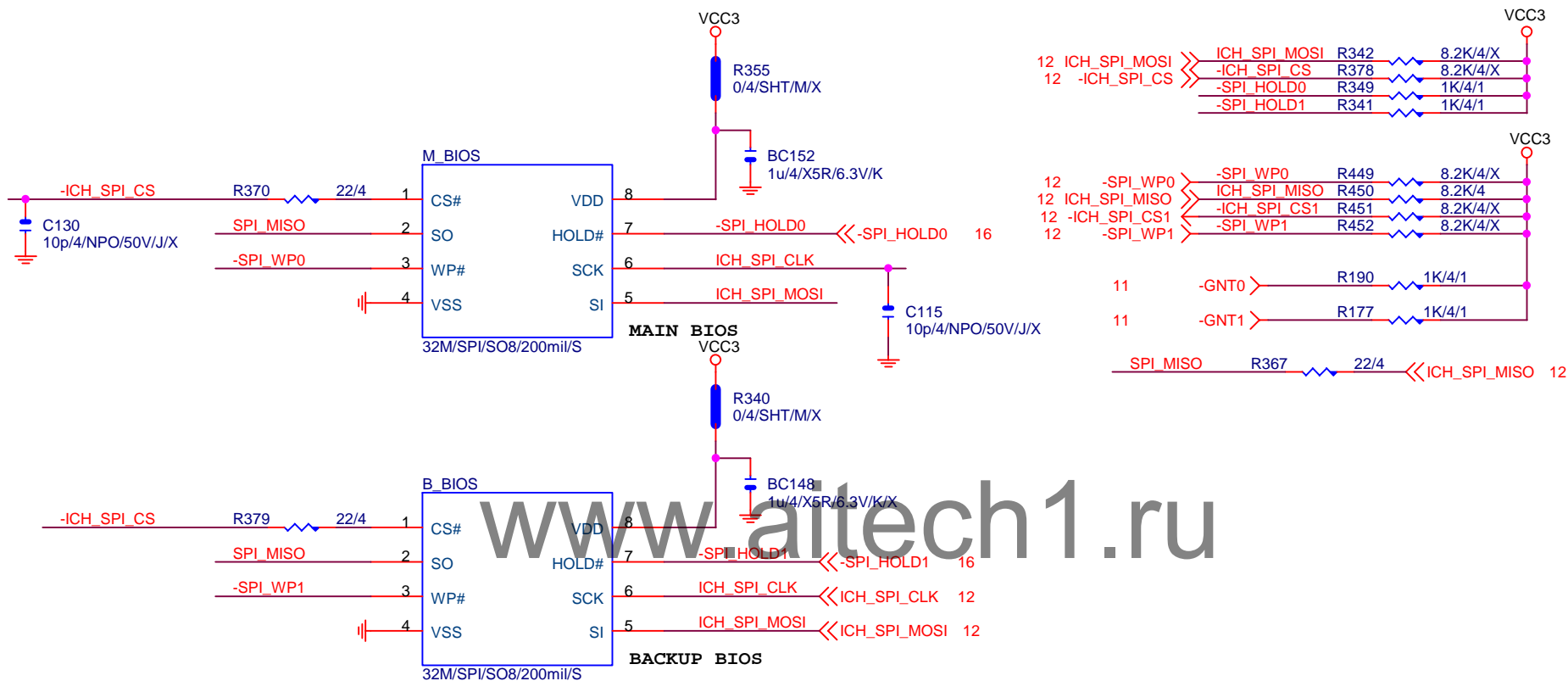








# DUAL BIOS



BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating  
0 means PD 1K

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Title

**DUAL BIOS**

Size  
A

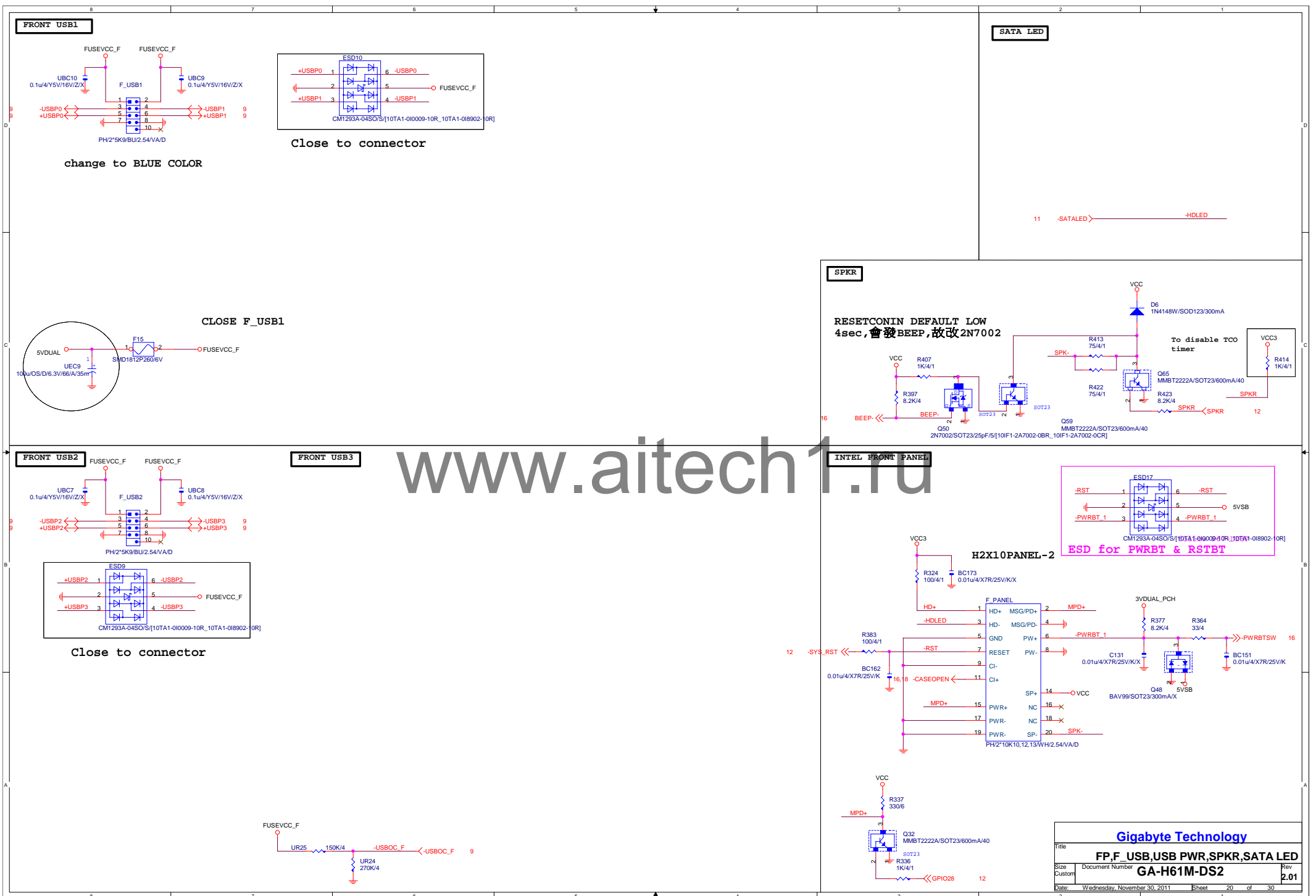
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**GA-H61M-DS2**

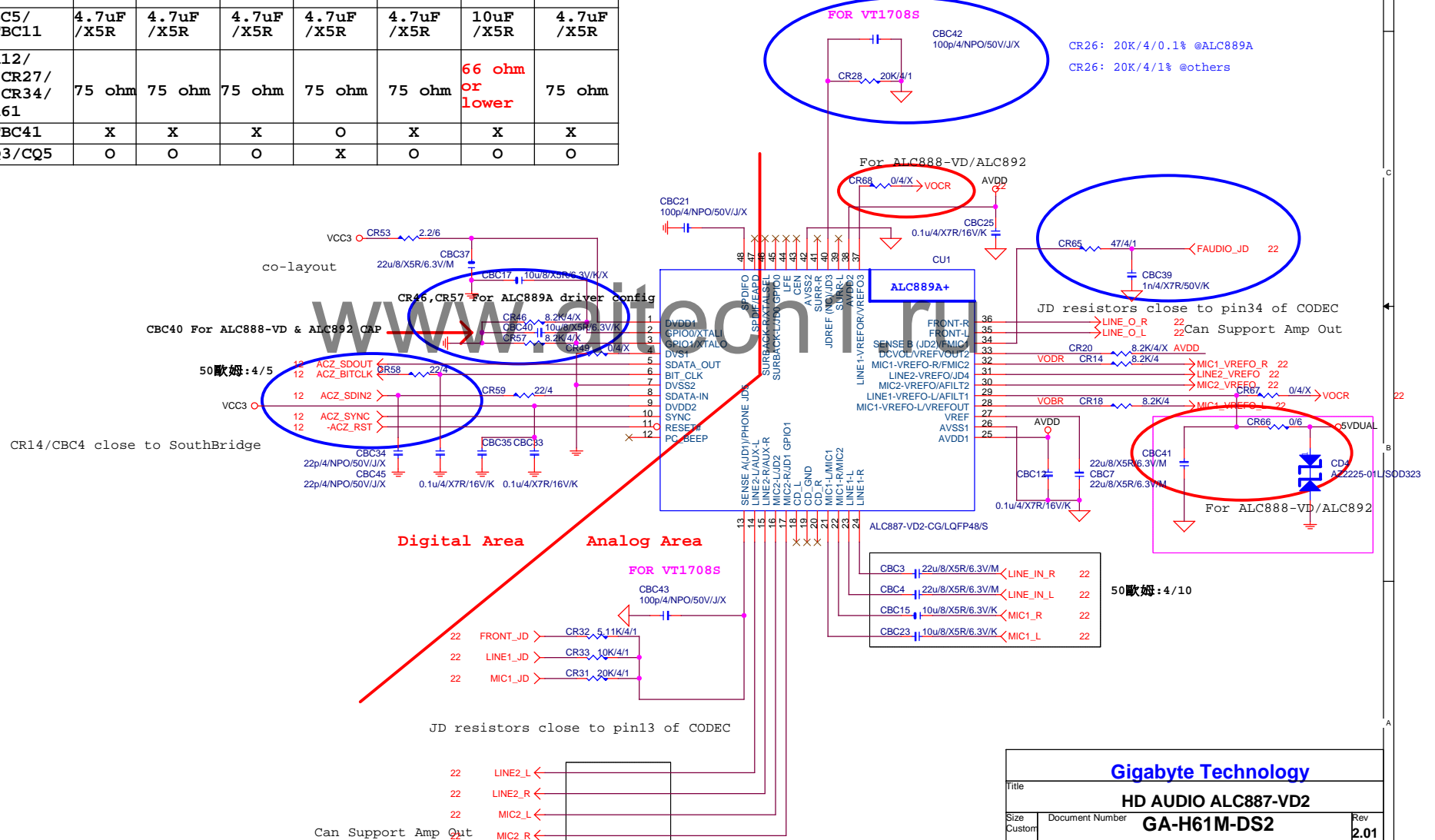
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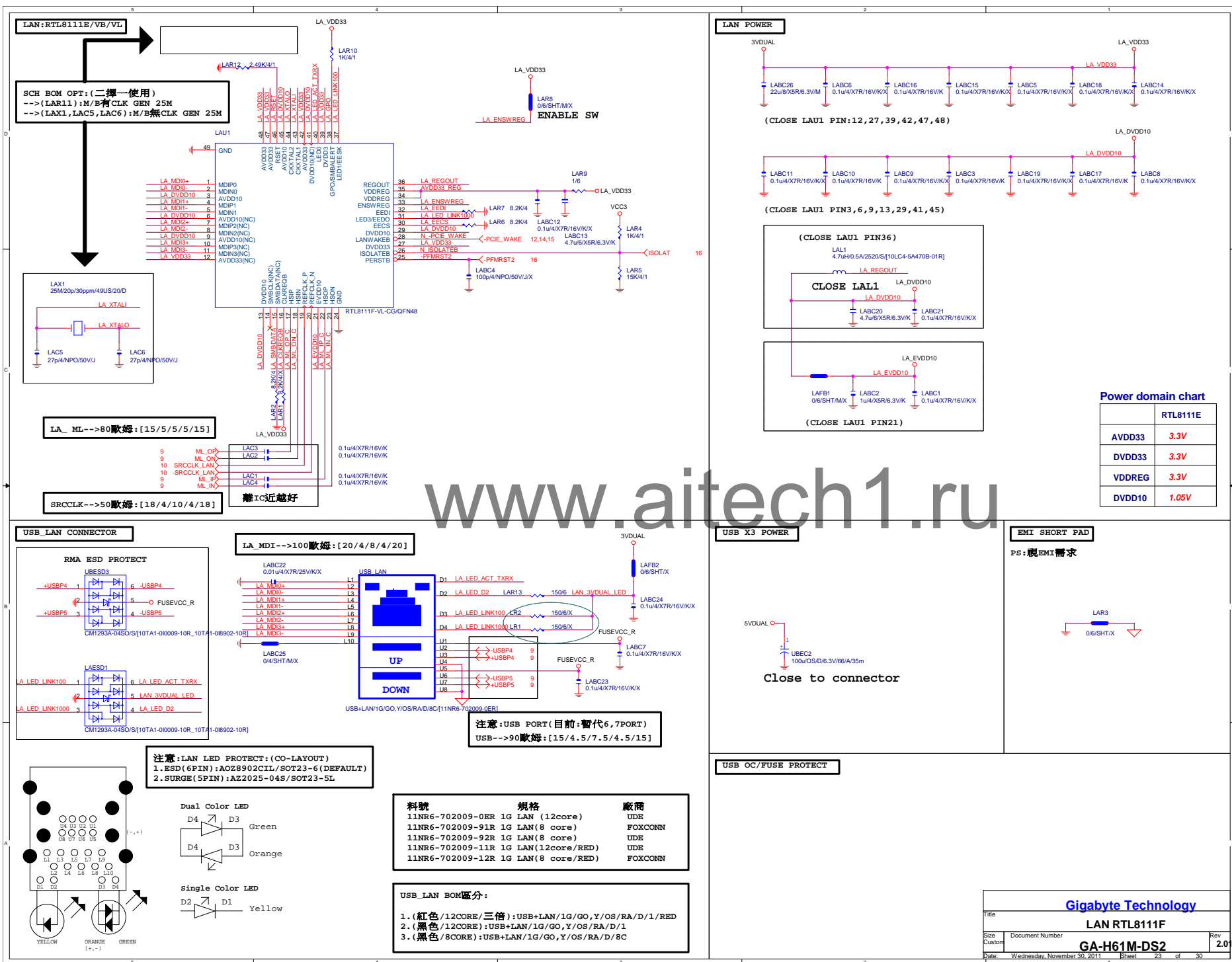
	ALC883	ALC888-VA	ALC888B	ALC888-VD	ALC892R	ALC889	ALC889A
CR46	X	X	X	X	X	X	O
CR57	X	X	X	X	X	X	O
CR49	O	O	X	X	X	O	O
CBC40	X	X	X	10uF/X5R	10uF/X5R	X	X
CR20	O	X	X	X	X	X	X
CR26	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/0.1%
CR47	X	X	O	X	O	O	X
CR48	O	O	X	O	X	X	O
CBC2/CBC4/CBC5/ CBC6/CBC10/CBC11	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	10uF /X5R	4.7uF /X5R
CR1/CR3/CR10/CR12/ CR15/CR19/CR56/CR27/ CR55/CR37/CR28/CR34/ CR6/CR9/CR51/CR61	75 ohm	75 ohm	75 ohm	75 ohm	75 ohm	66 ohm or lower	75 ohm
CR66/CR68/CD3/CBC41	X	X	X	O	X	X	X
CR67/CD1/CD2/CQ3/CQ5	O	O	O	X	O	O	O

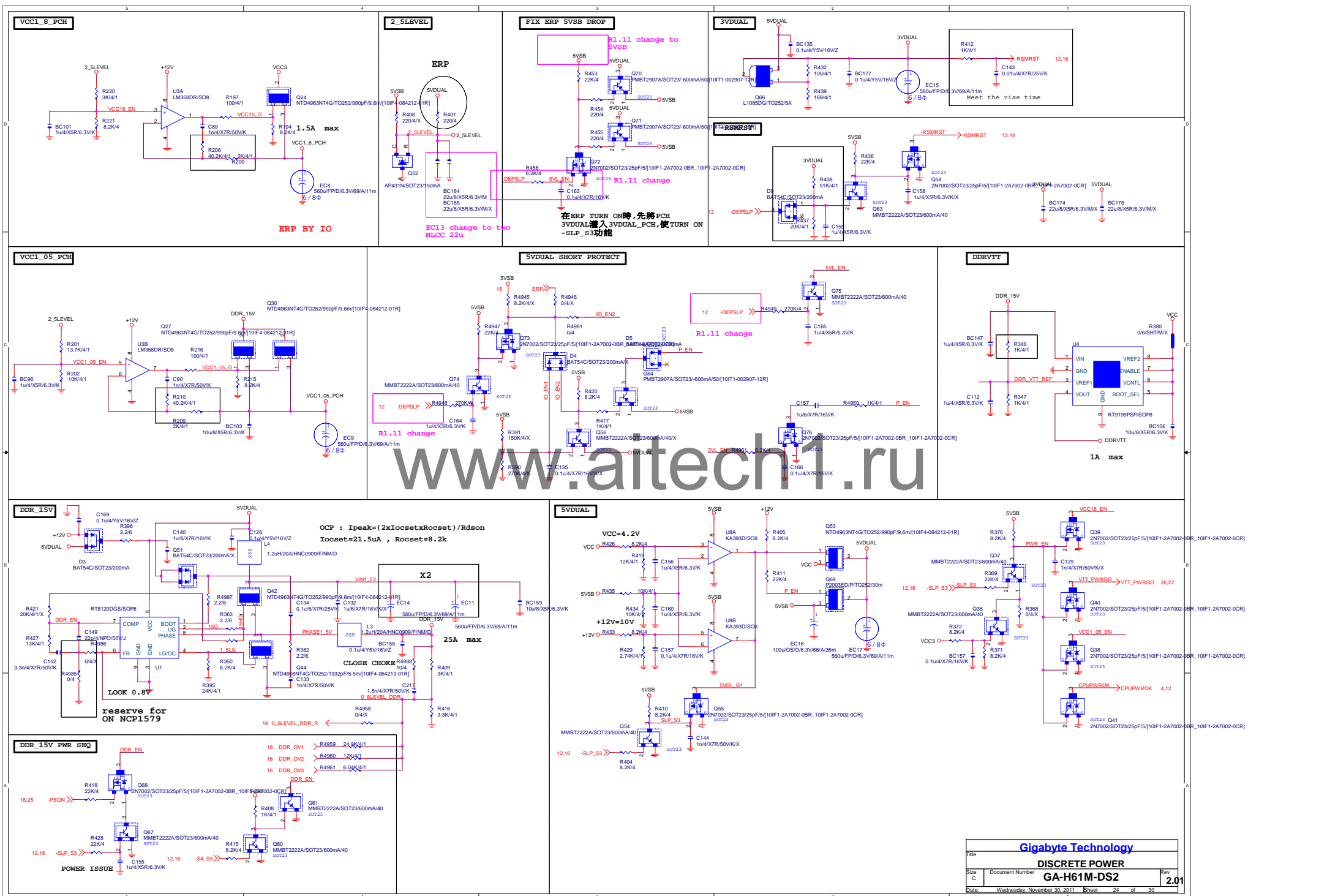


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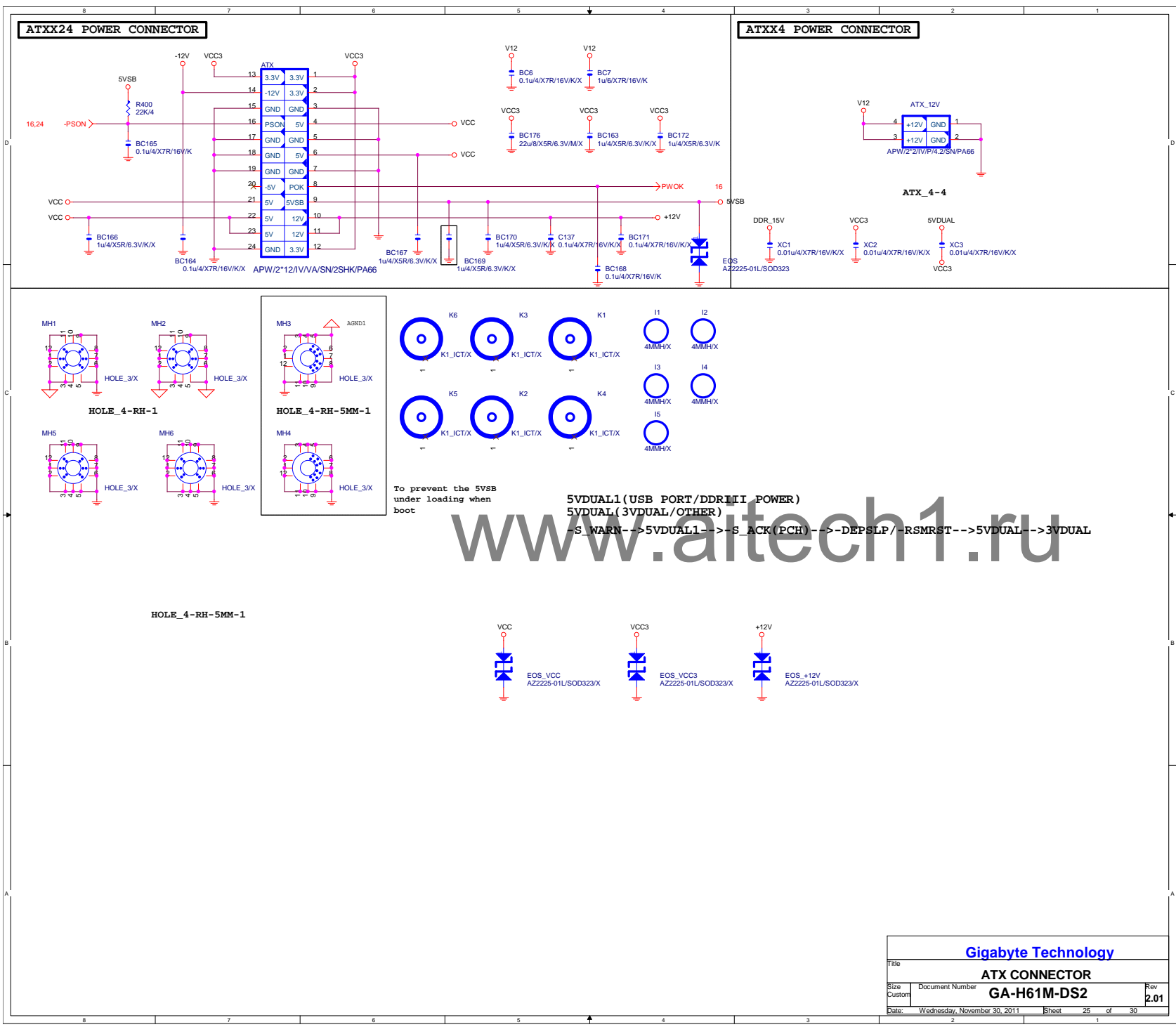
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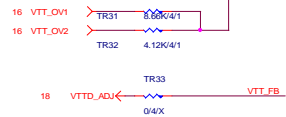




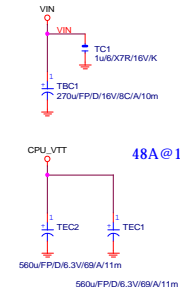
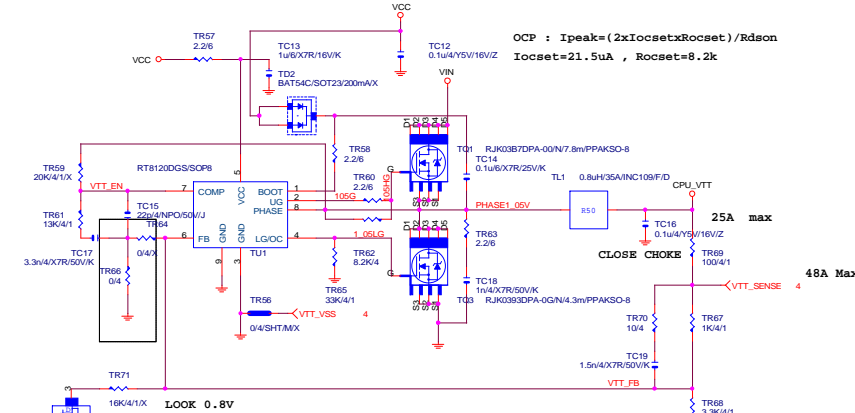


# CPU\_VTT

## VTT OV option



	VTT_EN
HI	ENABLE
LO	DISABLE

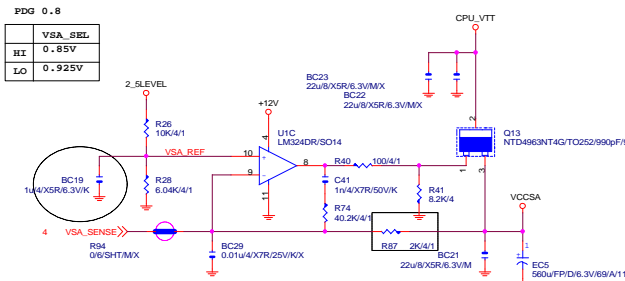


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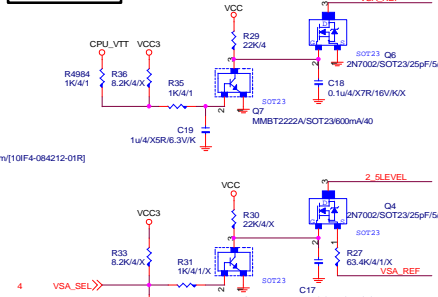
## VCCSA

PDG 0.8

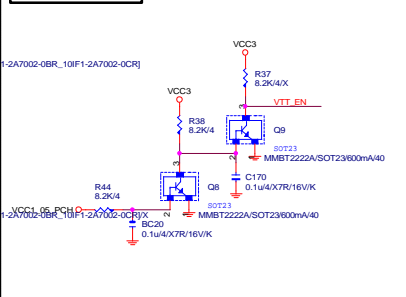
	VSA_SEL
HI	0.85V
LO	0.925V



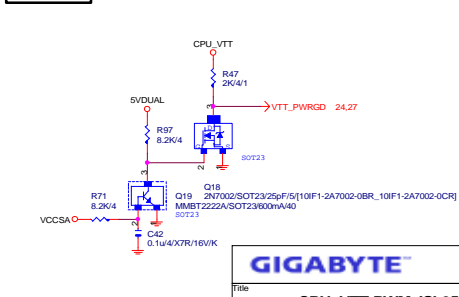
## VCCSA PWR SEQ



## CPU\_VTT PWR SEQ



## VTT\_PWRGD

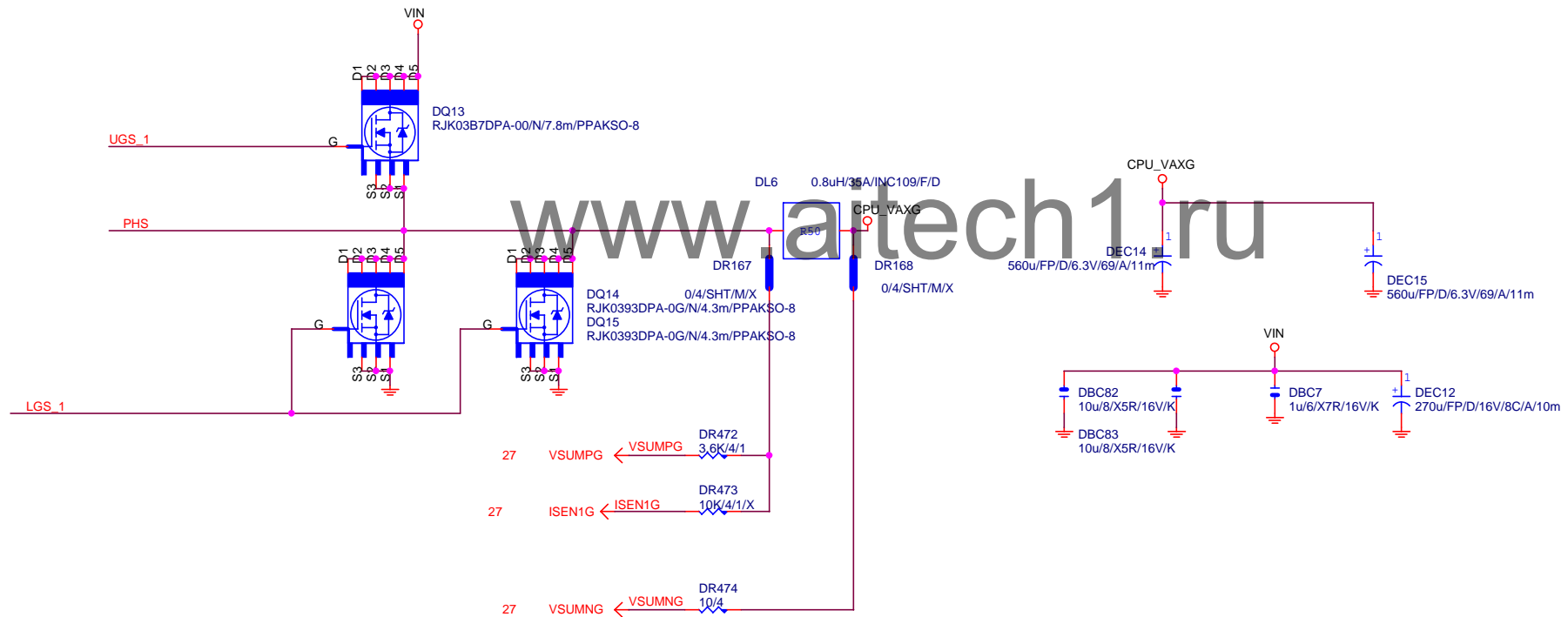
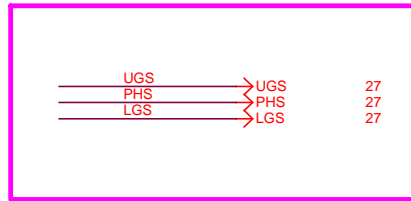


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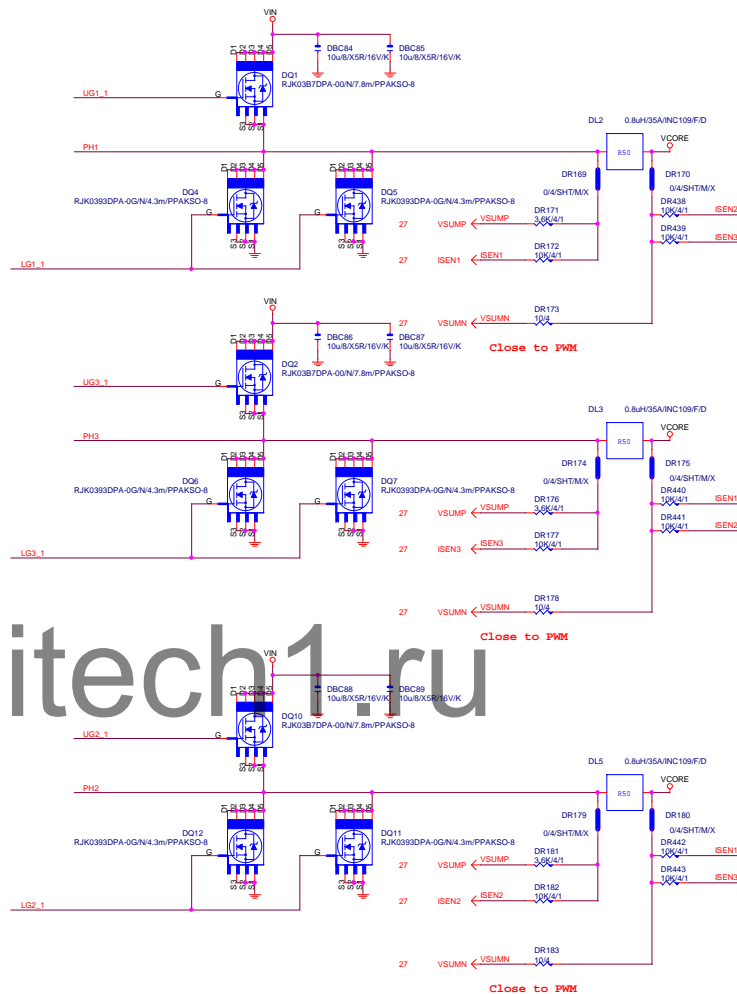
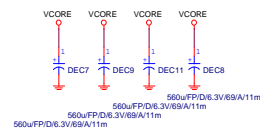
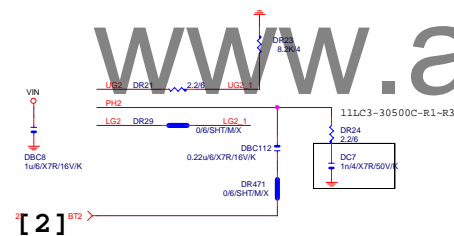
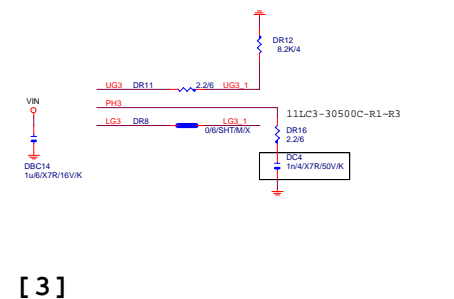
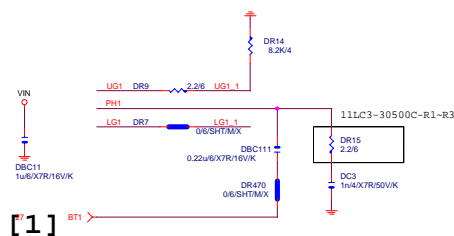
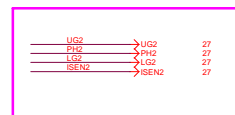
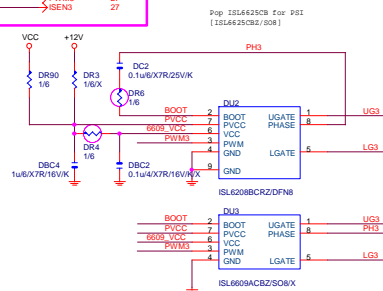


VAXG



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